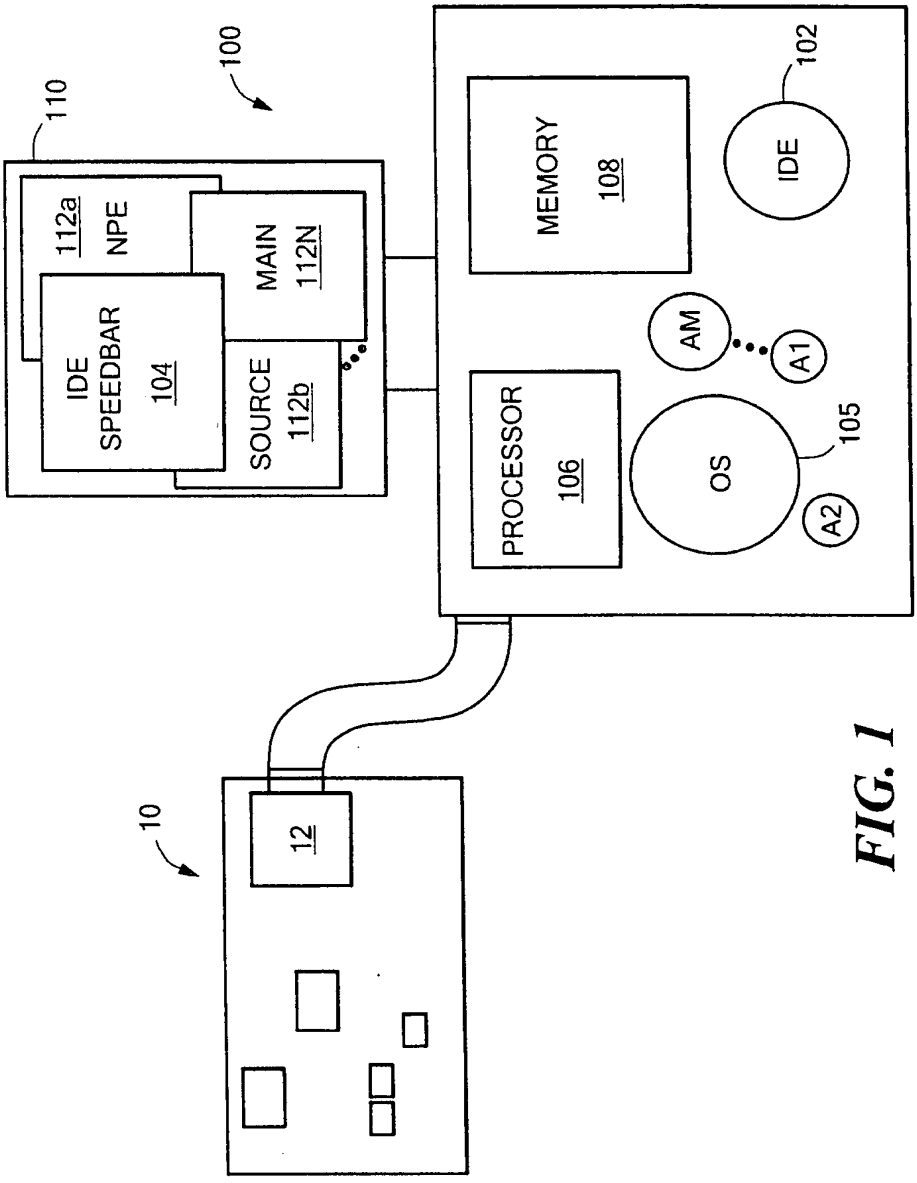

FIG. 1

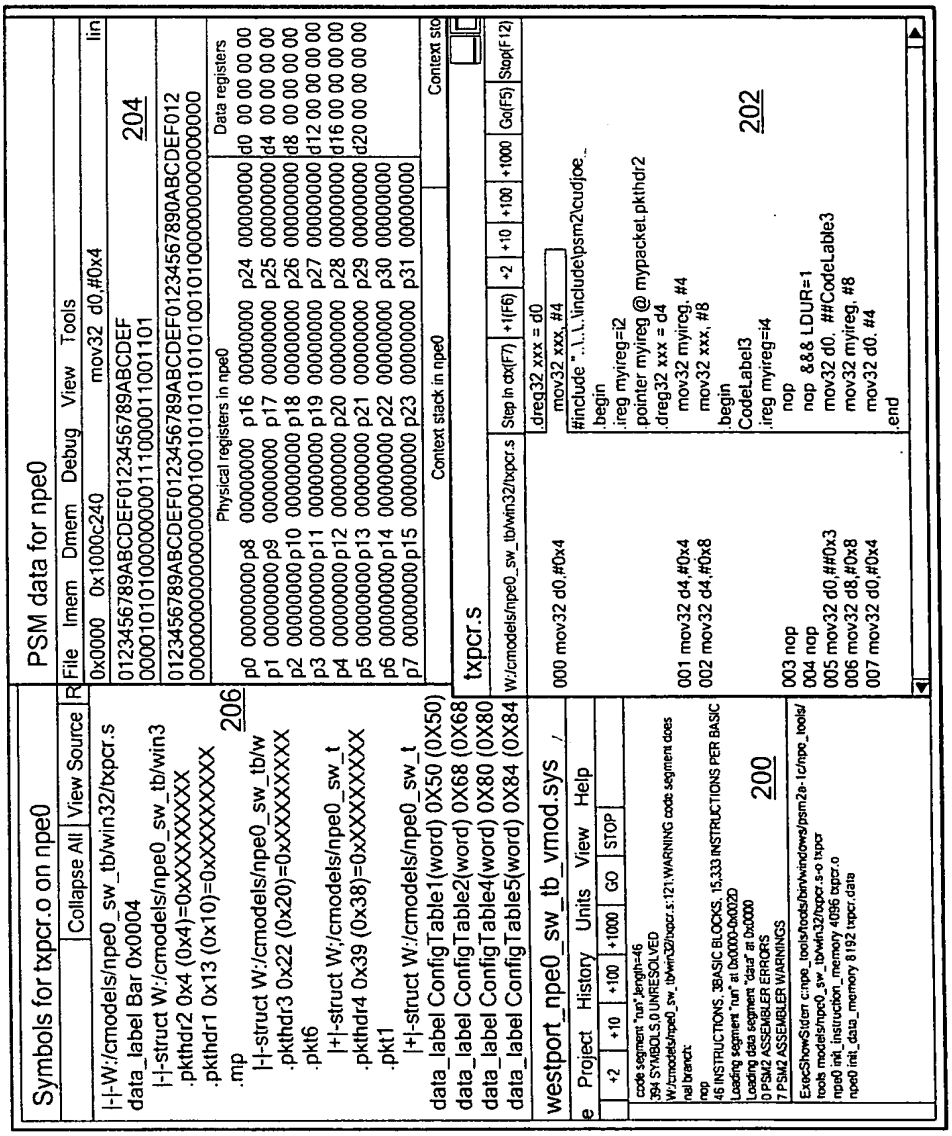
Symbols for txpcr.o on npe0
Collapse All
View Source
PSM data for npe0
File
Imem
Dmem
Debug
View
Tools
206
204
202
200
txpcr.s
Physical registers in npe0
Data registers
Context stack in npe0
Step in ctx(F7)
+1(F6)
+2
+10
+100
+1000
Go(F5)
Stop(F12)
westport_npe0_sw_tb_vmod.sys
Project
History
Units
View
Help
GO
STOP

FIG. 2

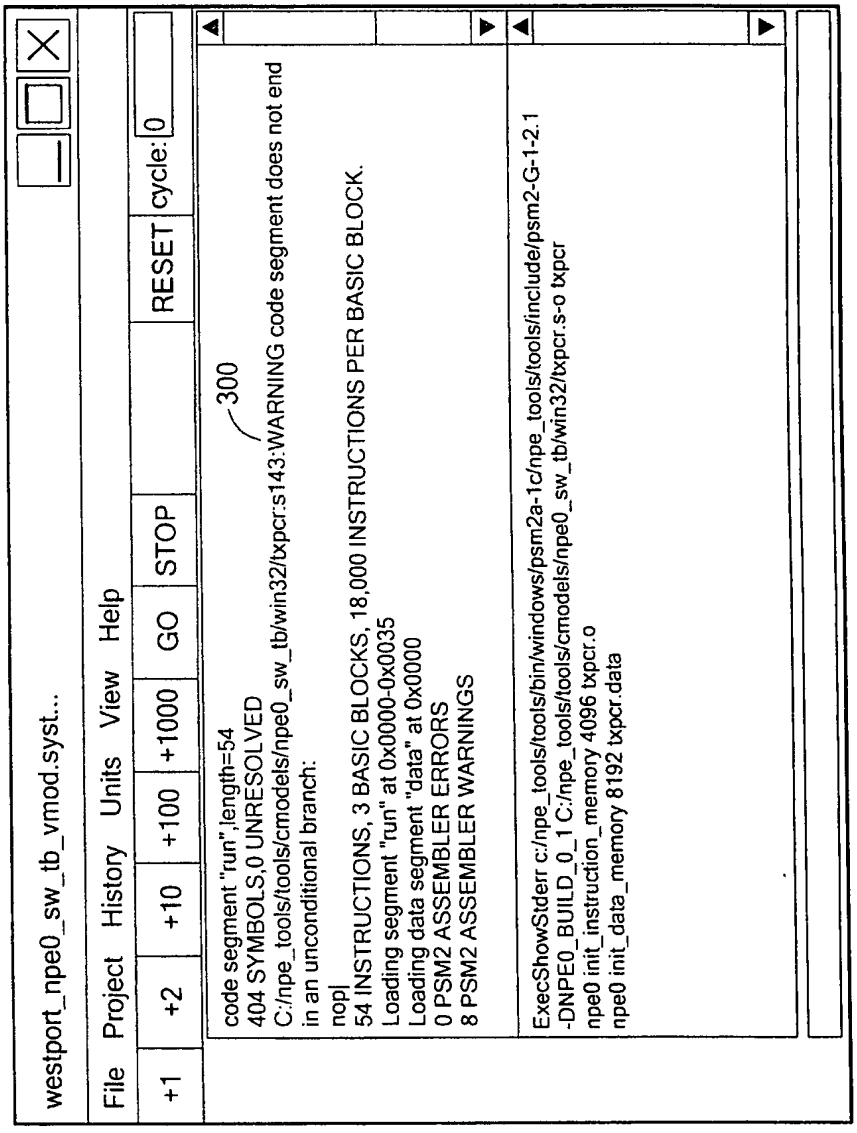
200
westport_npe0_sw_tb_vmod.syst...
File Project History Units View Help
+1 +2 +10 +100 +1000 GO STOP RESET cycle: 0
300
code segment "run",length=54
404 SYMBOLS,0 UNRESOLVED
C:/npe_tools/tools/cmodels/npe0_sw_tb/win32/txpcr:s143:WARNING code segment does not end in an unconditional branch:
nop|
54 INSTRUCTIONS, 3 BASIC BLOCKS, 18,000 INSTRUCTIONS PER BASIC BLOCK.
Loading segment "run" at 0x0000-0x0035
Loading data segment "data" at 0x0000
0 PSM2 ASSEMBLER ERRORS
8 PSM2 ASSEMBLER WARNINGS
ExecShowStderr c:/npe_tools/tools/bin/windows/psm2a-1c/npe_tools/tools/include/psm2-G-1-2.1 -DNPE0_BUILD_0_1 C:/npe_tools/tools/cmodels/npe0_sw_tb/win32/txpcr.s-o txpcr
npe0 init_instruction_memory 4096 txpcr.o
npe0 init_data_memory 8192 txpcr.data

FIG. 3

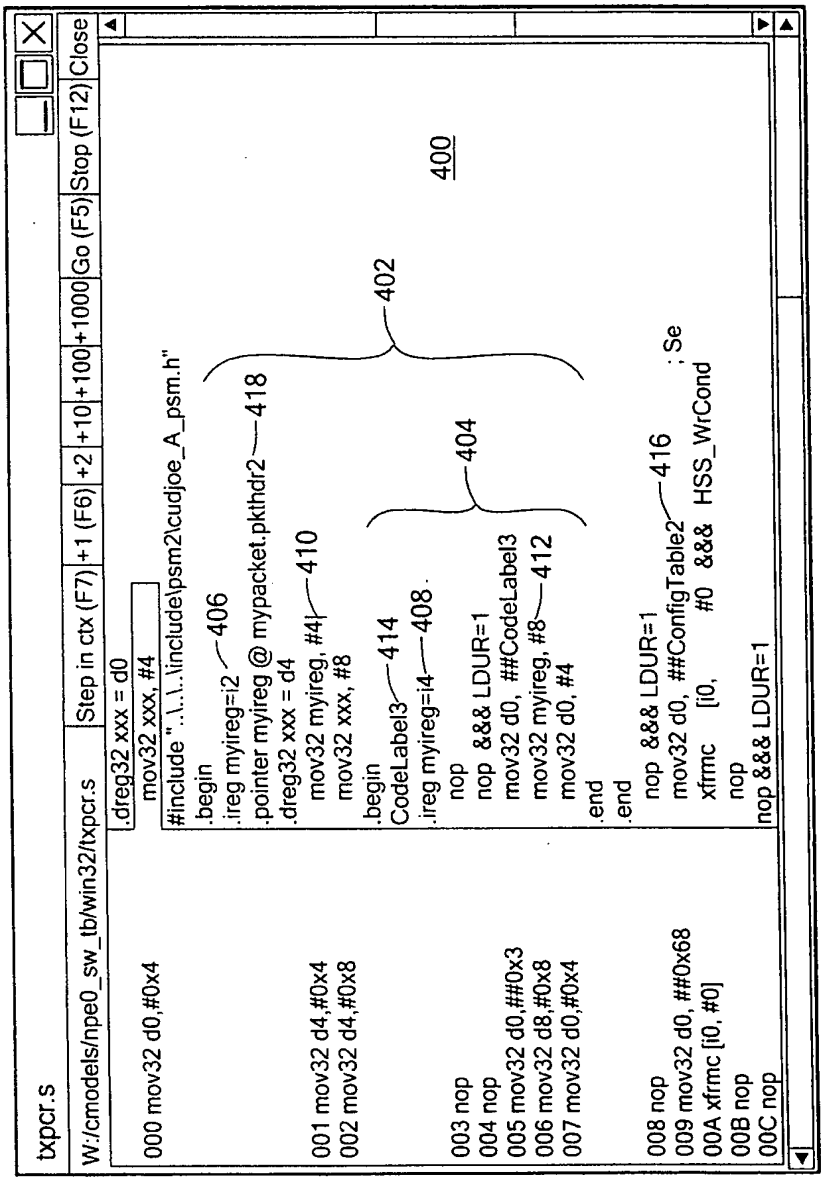
202
txpcr.s
W:/cmodels/npe0_sw_tb/win32/txpcr.s
Step in ctx (F7)
+1 (F6)
+2
+10
+100
+1000
Go (F5)
Stop (F12)
Close
.dreg32 xxx = d0
mov32 xxx, #4
#include "..\..\..\include\psm2\cudjoe_A_psm.h"
.begin
.ireg myireg=i2
.pointer myireg @ mypacket.pkthdr2
.dreg32 xxx = d4
mov32 myireg, #4
mov32 xxx, #8
.begin
CodeLabel3
.ireg myireg=i4
nop
nop &&& LDUR=1
mov32 d0, ##CodeLabel3
mov32 myireg, #8
mov32 d0, #4
.end
.end
nop &&& LDUR=1
mov32 d0, ##ConfigTable2
xfrmc [i0, #0 &&& HSS_WrCond
nop
nop &&& LDUR=1
000 mov32 d0,#0x4
001 mov32 d4,#0x4
002 mov32 d4,#0x8
003 nop
004 nop
005 mov32 d0,##0x3
006 mov32 d8,#0x8
007 mov32 d0,#0x4
008 nop
009 mov32 d0, ##0x68
00A xfrmc [i0, #0]
00B nop
00C nop
406
418
410
414
408
404
412
416
402
400

FIG. 4

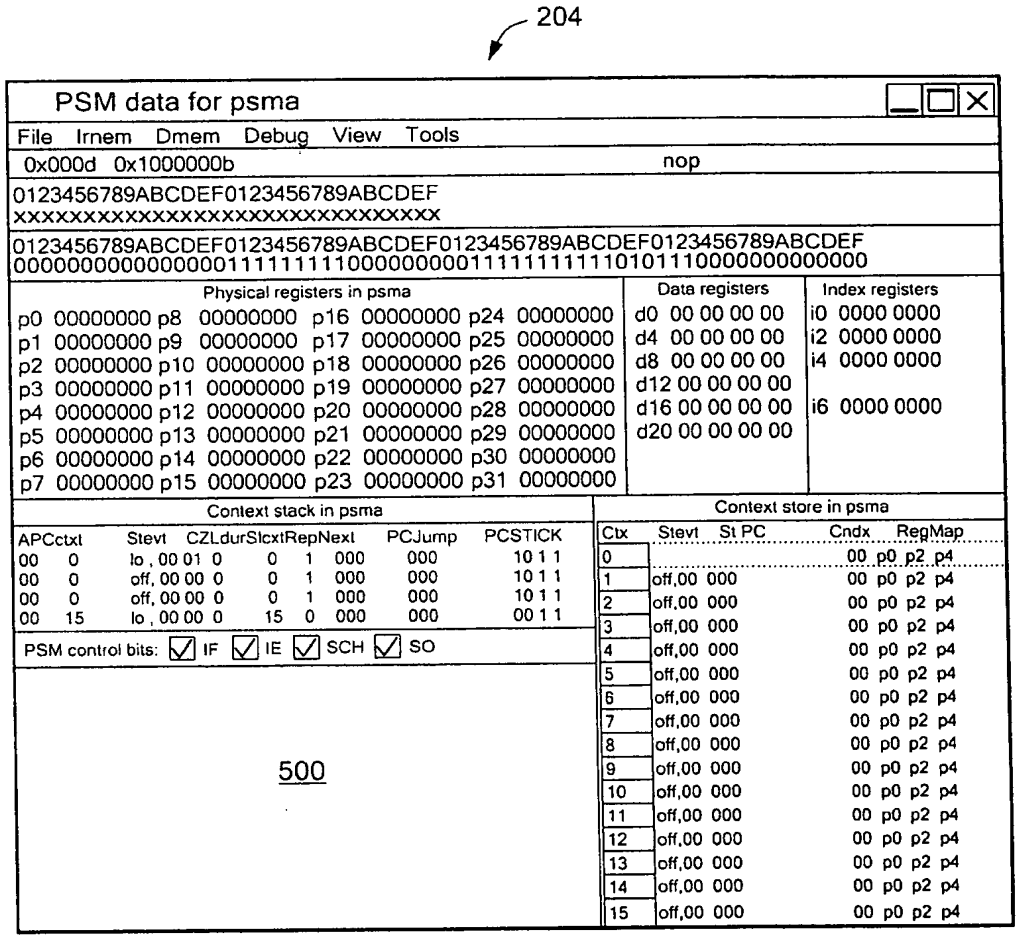
204
PSM data for psma
File Irnem Dmem Debug View Tools
0x000d 0x1000000b
nop
0123456789ABCDEF0123456789ABCDEF
xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx
0123456789ABCDEF0123456789ABCDEF0123456789ABCDEF0123456789ABCDEF
00000000000000001111111110000000001111111111101011100000000000
Physical registers in psma
p0 00000000 p8 00000000 p16 00000000 p24 00000000
p1 00000000 p9 00000000 p17 00000000 p25 00000000
p2 00000000 p10 00000000 p18 00000000 p26 00000000
p3 00000000 p11 00000000 p19 00000000 p27 00000000
p4 00000000 p12 00000000 p20 00000000 p28 00000000
p5 00000000 p13 00000000 p21 00000000 p29 00000000
p6 00000000 p14 00000000 p22 00000000 p30 00000000
p7 00000000 p15 00000000 p23 00000000 p31 00000000
Data registers
d0 00 00 00 00
d4 00 00 00 00
d8 00 00 00 00
d12 00 00 00 00
d16 00 00 00 00
d20 00 00 00 00
Index registers
i0 0000 0000
i2 0000 0000
i4 0000 0000
i6 0000 0000
Context stack in psma
APCctxt Stevt CZLdurStcxtRepNext PCJump PCSTICK
00 0 lo , 00 01 0 0 1 000 000 10 1 1
00 0 off, 00 00 0 0 1 000 000 10 1 1
00 0 off, 00 00 0 0 1 000 000 10 1 1
00 15 lo , 00 00 0 15 0 000 000 00 1 1
PSM control bits: IF IE SCH SO
500
Context store in psma
Ctx Stevt St PC Cndx RegMap
0 00 p0 p2 p4
1 off,00 000 00 p0 p2 p4
2 off,00 000 00 p0 p2 p4
3 off,00 000 00 p0 p2 p4
4 off,00 000 00 p0 p2 p4
5 off,00 000 00 p0 p2 p4
6 off,00 000 00 p0 p2 p4
7 off,00 000 00 p0 p2 p4
8 off,00 000 00 p0 p2 p4
9 off,00 000 00 p0 p2 p4
10 off,00 000 00 p0 p2 p4
11 off,00 000 00 p0 p2 p4
12 off,00 000 00 p0 p2 p4
13 off,00 000 00 p0 p2 p4
14 off,00 000 00 p0 p2 p4
15 off,00 000 00 p0 p2 p4

FIG. 5

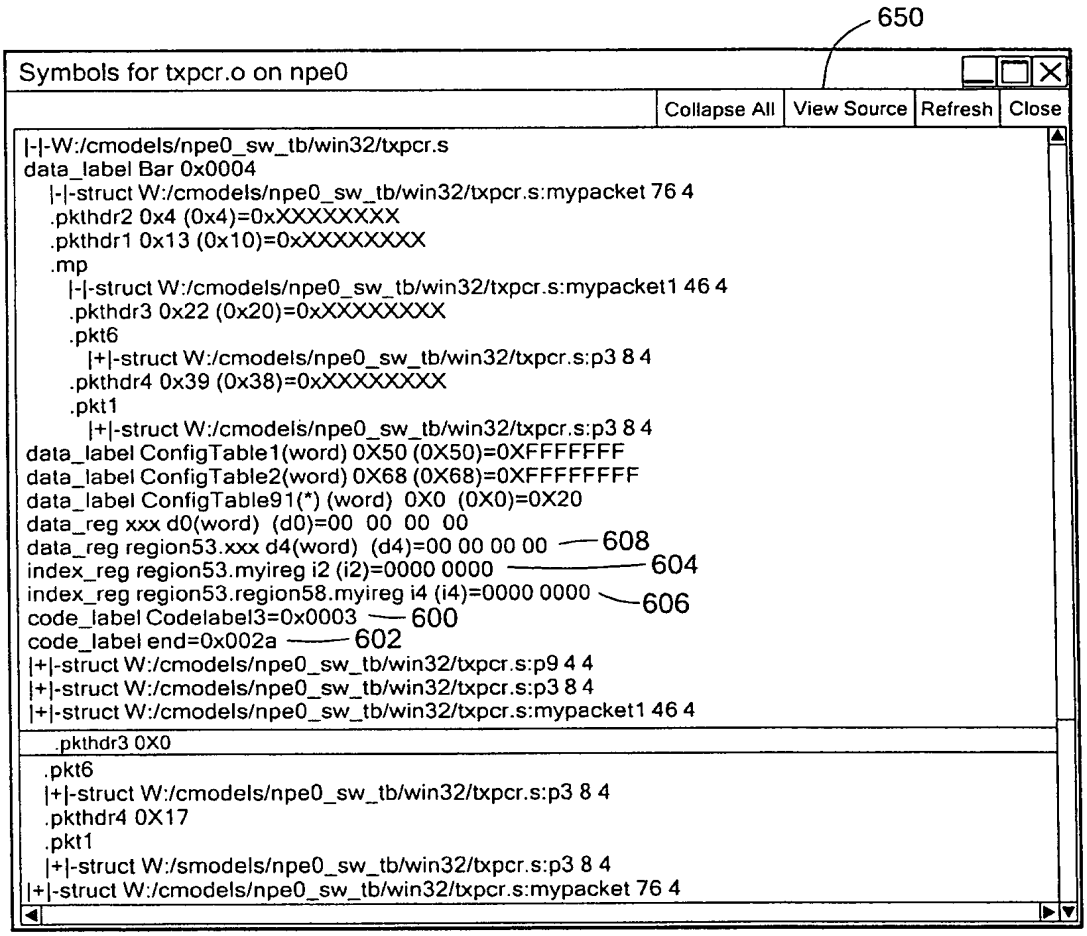
650
Symbols for txpcr.o on npe0
Collapse All
View Source
Refresh
Close
|-|-W:/cmodels/npe0_sw_tb/win32/txpcr.s
data_label Bar 0x0004
|-|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket 76 4
.pkthdr2 0x4 (0x4)=0xXXXXXXXX
.pkthdr1 0x13 (0x10)=0xXXXXXXXX
.mp
|-|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket1 46 4
.pkthdr3 0x22 (0x20)=0xXXXXXXXX
.pkt6
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
.pkthdr4 0x39 (0x38)=0xXXXXXXXX
.pkt1
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
data_label ConfigTable1(word) 0X50 (0X50)=0XFFFFFFFF
data_label ConfigTable2(word) 0X68 (0X68)=0XFFFFFFFF
data_label ConfigTable91(*) (word) 0X0 (0X0)=0X20
data_reg xxx d0(word) (d0)=00 00 00 00
data_reg region53.xxx d4(word) (d4)=00 00 00 00
608
index_reg region53.myireg i2 (i2)=0000 0000
604
index_reg region53.region58.myireg i4 (i4)=0000 0000
606
code_label Codelabel3=0x0003
600
code_label end=0x002a
602
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p9 4 4
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket1 46 4
.pkthdr3 0X0
.pkt6
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
.pkthdr4 0X17
.pkt1
|+|-struct W:/smodels/npe0_sw_tb/win32/txpcr.s:p3 8 4
|+|-struct W:/cmodels/npe0_sw_tb/win32/txpcr.s:mypacket 76 4

FIG. 6

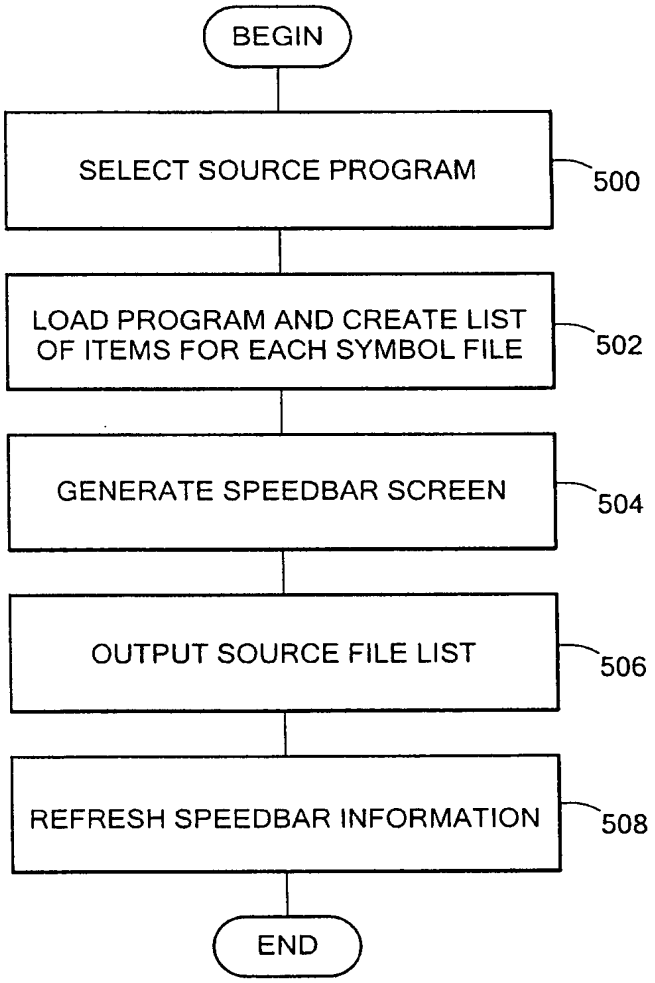
BEGIN
SELECT SOURCE PROGRAM
500
LOAD PROGRAM AND CREATE LIST OF ITEMS FOR EACH SYMBOL FILE
502
GENERATE SPEEDBAR SCREEN
504
OUTPUT SOURCE FILE LIST
506
REFRESH SPEEDBAR INFORMATION
508
END

FIG. 7

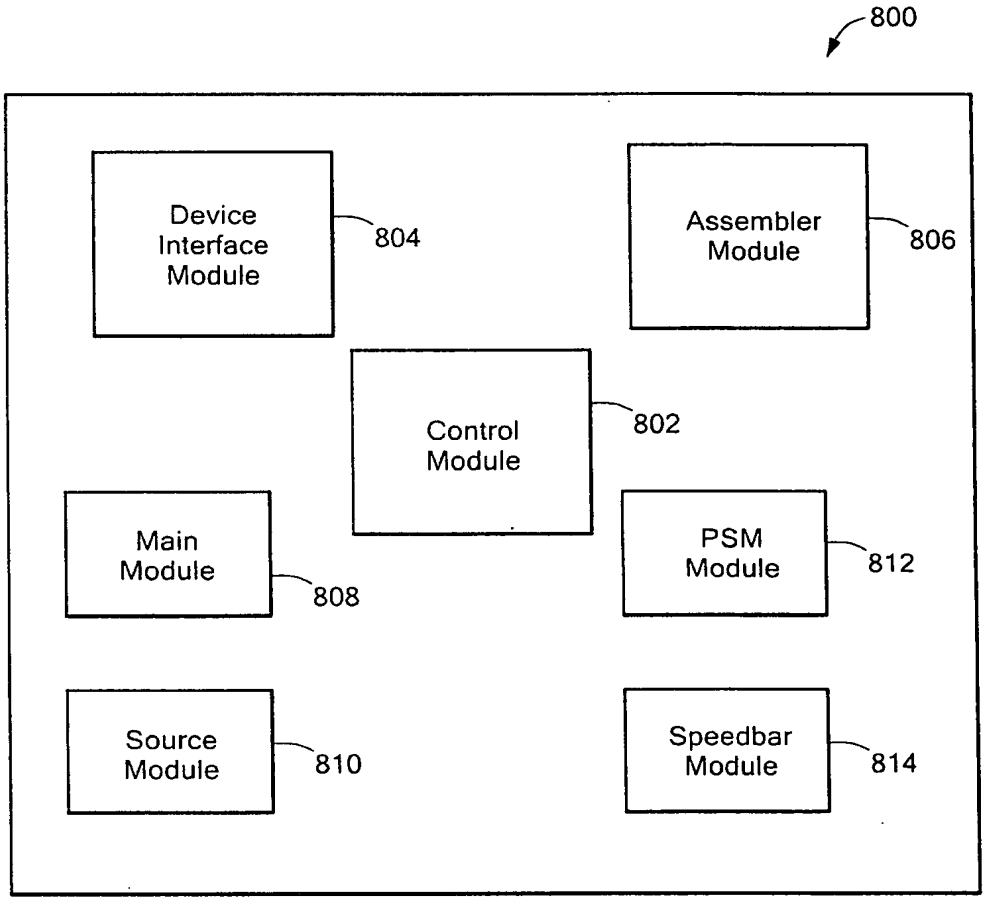
800
Device Interface Module
804
Assembler Module
806
Control Module
802
Main Module
808
PSM Module
812
Source Module
810
Speedbar Module
814

FIG. 8